FIG. 1

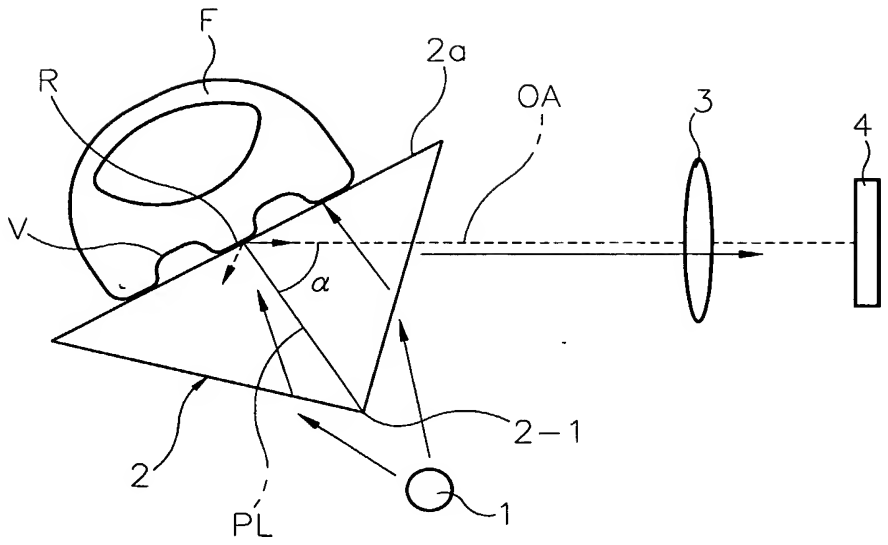


FIG.2

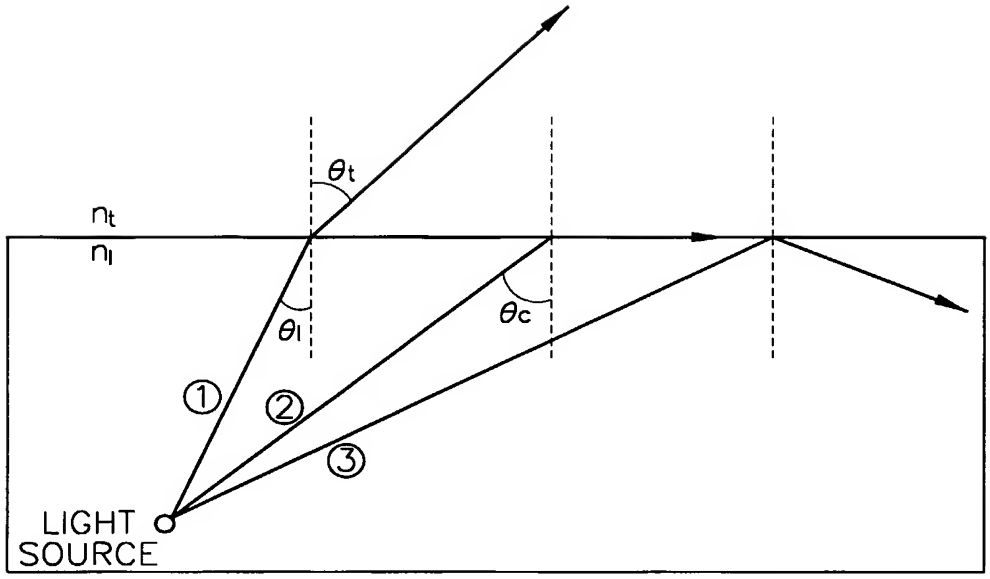
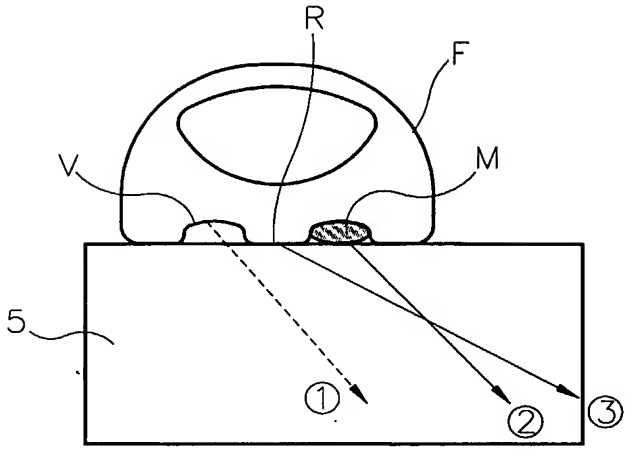
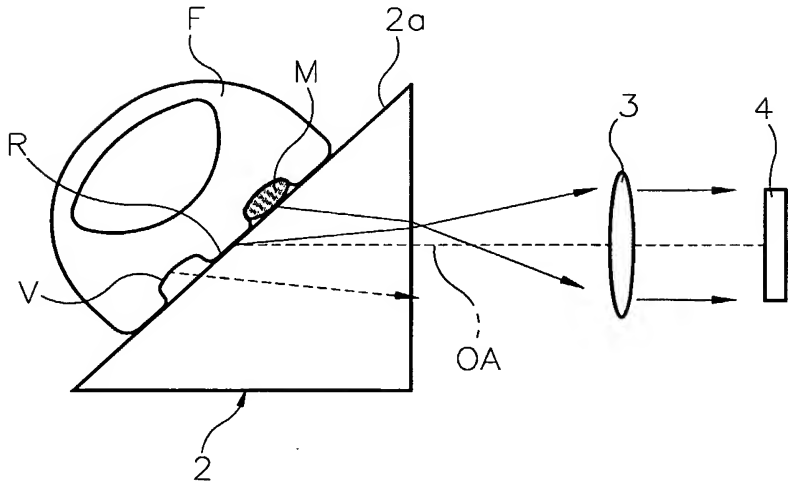


FIG.3

(a)



(b)



(c)

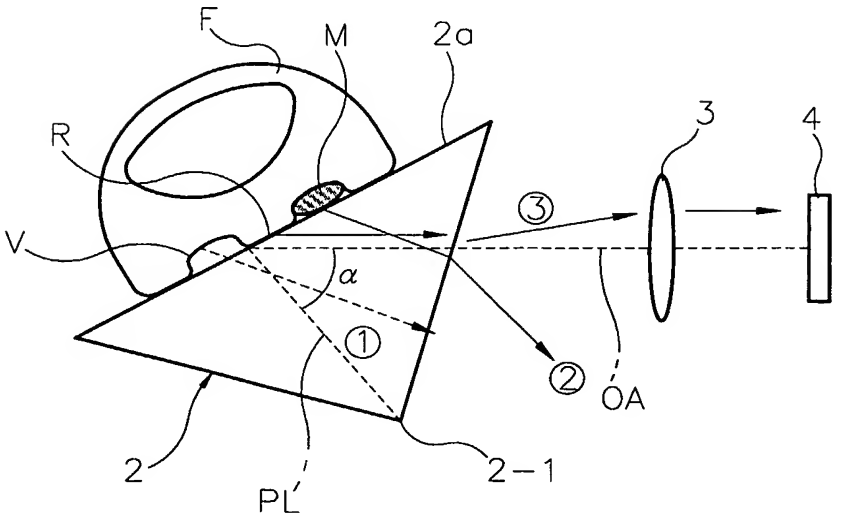


FIG.4

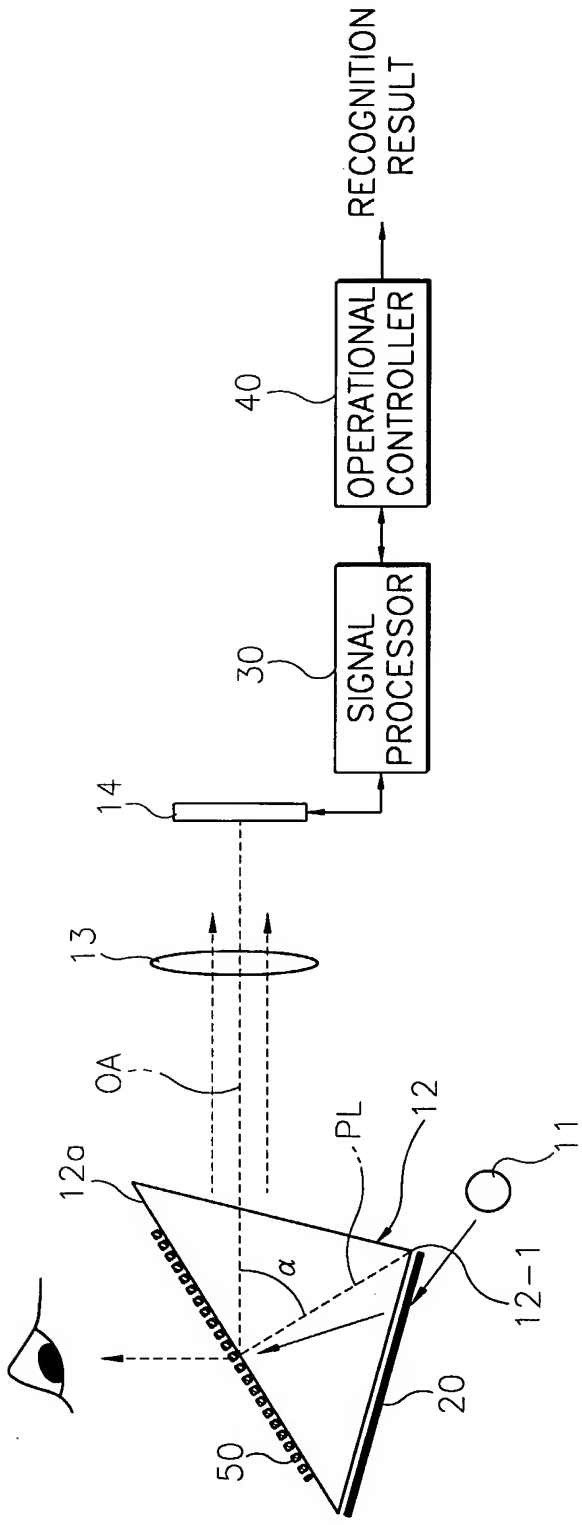
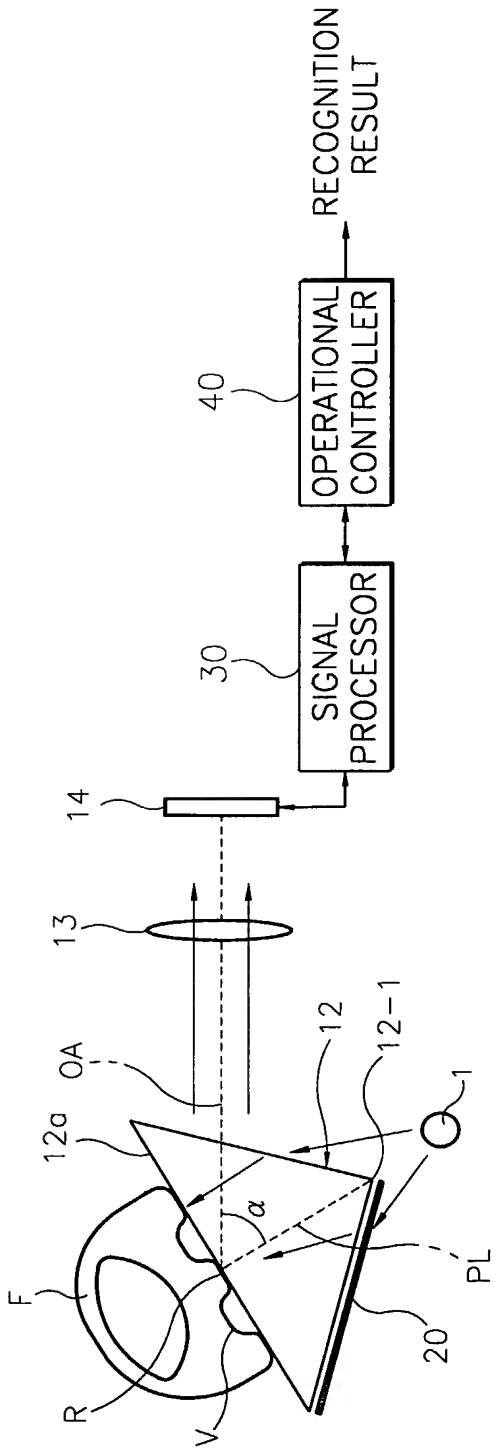

RECOGNITION RESULT
OPERATIONAL CONTROLLER
40
SIGNAL PROCESSOR
30
14
13
OA
PL
12
12a
11
12-1
α
20
50

FIG.5


RECOGNITION RESULT
OPERATIONAL CONTROLLER
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SIGNAL PROCESSOR
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14
13
OA
12
12-1
12a
1
α
F
R
V
PL
20